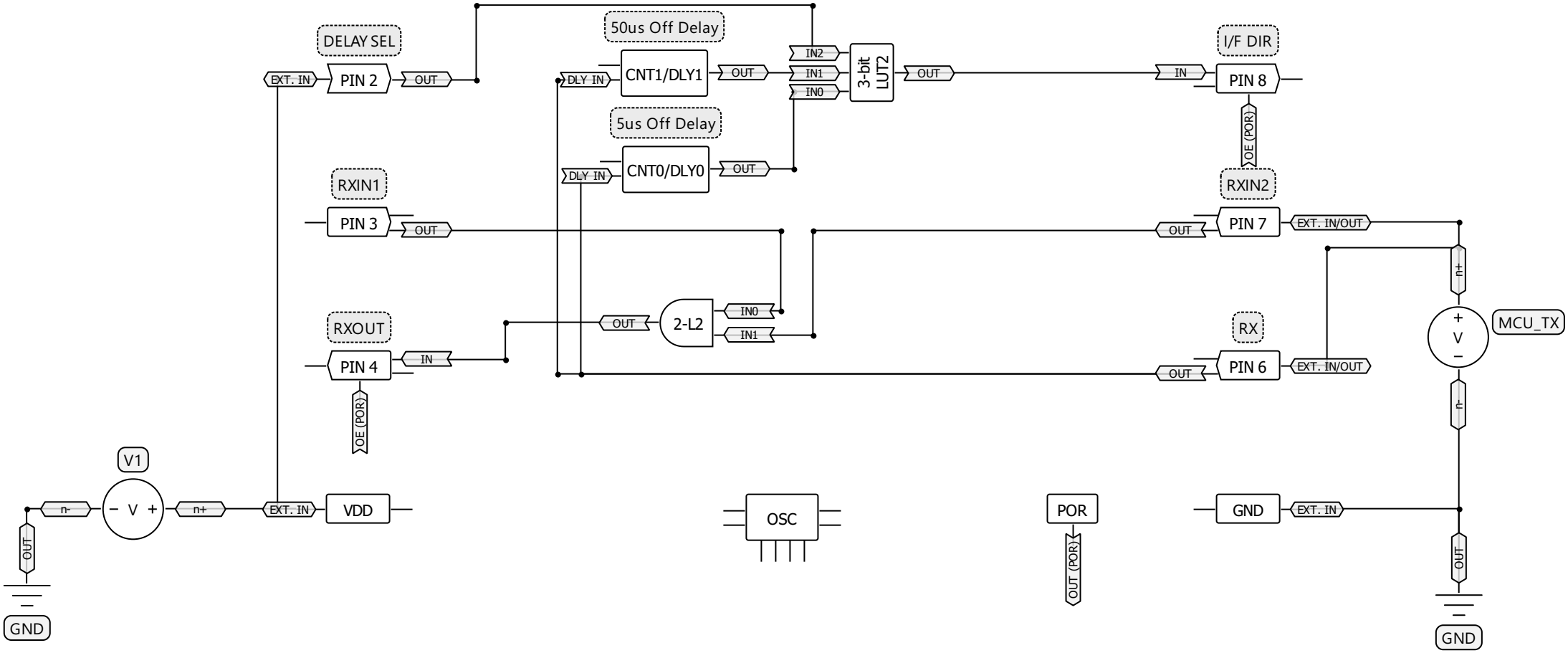
DELAY SEL
EXT. IN
PIN 2
OUT
50us Off Delay
DLY IN
CNT1/DLY1
OUT
IN2
IN1
IN0
3-bit LUT2
OUT
I/F DIR
IN
PIN 8
OE (POR)
5us Off Delay
DLY IN
CNT0/DLY0
OUT
RXIN1
PIN 3
OUT
RXIN2
OUT
PIN 7
EXT. IN/OUT
RXOUT
PIN 4
IN
OUT
2-L2
IN0
IN1
RX
OUT
PIN 6
EXT. IN/OUT
OE (POR)
V1
n-
V
n+
EXT. IN
VDD
OSC
POR
OUT (POR)
GND
EXT. IN
n+
V
n-
MCU_TX
OUT
GND
OUT
GND

VDD (PIN 1)

Property	*Value*
Min. value (V)	2.80
Typ. value (V)	3.30
Max. value (V)	5.20

PIN 2
Label: "DELAY SEL"

Property	*Value*
I/O selection	Digital input
In mode	Digital in without Schmitt trigger
Out mode	None
Resistor	Pull Down
Resistor value	100K
Reset mode	Disable

PIN 3
Label: "RXIN1"

Property	*Value*
I/O selection	Digital input
In mode	Digital in with Schmitt trigger
Out mode	None
Resistor	Pull Up
Resistor value	10K

PIN 4
Label: "RXOUT"

Property	*Value*
I/O selection	Digital output
In mode	None
Out mode	2x push pull

PIN 6
Label: "RX"

Property	*Value*
I/O selection	Digital input
In mode	Digital in with Schmitt trigger
Out mode	None
Resistor	Pull Up
Resistor value	100K

PIN 7
Label: "RXIN2"

Property	*Value*
I/O selection	Digital input
In mode	Digital in with Schmitt trigger
Out mode	None
Resistor	Pull Up
Resistor value	10K

PIN 8
Label: "I/F DIR"

Property	*Value*
I/O selection	Digital output
In mode	None
Out mode	1x push pull

2-bit LUT2

IN1	*IN0*	*OUT*
0	0	0
0	1	0
1	0	0
1	1	1

Property	*Value*
Standard gates	AND

3-bit LUT2

IN2	*IN1*	*IN0*	*OUT*
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	0

Property	*Value*
Standard gates	Defined by user

8-bit CNT0/DLY0
Label: "5us Off Delay"

Property	*Value*
Mode	Delay
Counter data	9
Delay time (typical)	5 us
Edge select	Rising
Clock	CLK
Clock frequency	2 MHz

8-bit CNT1/DLY1
Label: "50us Off Delay"

Property	*Value*
Mode	Delay
Counter data	99
Delay time (typical)	50 us
Edge select	Rising
Clock	CLK
Clock frequency	2 MHz

OSC	
Property	*Value*
OSC power mode	Auto power on
Clock selector	RC OSC
Frequency	2 MHz
'CLK' predivider	1
'OUT0' second divider	1
'OUT1' second divider	1
Fast start-up	Enable

External Components

V1	
Property	*Value*
Element	Voltage Source
Pre-start delay	0s
Internal capacitance	100nF
Internal resistance	10Ohm
Pre-start state	Low
Type	DC
DC Voltage	3.3V
Ramp rise time	1ms

MCU_TX	
Property	*Value*
Element	Voltage Source
Pre-start delay	0s
Internal capacitance	0F
Internal resistance	0Ohm
Repeat state	Cyclic
Pre-start state	Low
Type	Logic pattern
Mode	Normal
Umax	4V
Umin	0V
Levels adjustment	Standard
Rise time	1µs
Fall time	1µs

MCU_TX Pattern Points	
Duration	*Voltage*
200µs	4V
100µs	0V

Project Specs			
	Min.	*Typ.*	*Max.*
VDD (V):	2.80	3.30	5.20
Temperature (°C):	-10.00	25.00	55.00

General Settings	
GPIO quick charge	Disable
Pattern ID	1
Lock status	Unlocked